INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

PTO/SB/08A (10-01)
FEB 17 2004
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ATTY. DOCKET NO.	SERIAL NO.
CISCO-7563	10/724,559
APPLICANT	
Krischer et al.	
FILING DATE	**GROUP**
26 Nov 2003	2663

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
	AA	6,487,178 B1	Nov. 26, 2002	Romesburg et al.	370	286	May 12, 1999
	AB	2003/0009624 A1	Jan. 9, 2003	Gruner et al.	711	119	Mar. 25, 2002
	AC	2003/0041163 A1	Feb. 27, 2003	Rhoades et al.	709	232	Feb. 14, 2002
	AD	2003/0065832 A1	Apr. 3, 2003	Emerson et al.	710	23	Sep. 28, 2001
	AE	6,253,271 B1	Jun. 26, 2001	Ram et al.	710	128	Aug. 11, 1999
	AF	6,263,374 B1	Jul. 17, 2001	Olnowich et al.	709	253	Aug. 4, 1994
	AG	RE 37,980 E	Feb. 4, 2003	Elkhoury et al.	710	310	Feb. 4, 2003
	AH						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLI-CATION DATE	COUNTRY	CLASS	SUB-CLASS	TRANS-LATION YES	NO
	AI							
	AJ							

OTHER DISCLOSURES (Including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)

	AK	J. Stokes. "Understanding Bandwidth and Latency." Downloaded May 22, 2003 from Ars Technica. URL: http://www.arstechnica.com/paedia/b/bandwidth-latency/bandwidth-latency-1.html
	AL	"SOC-it." 2002. MIPS Technologies, Mountain View, CA. URL: http://www.mips.com/ProductCatalog/P_SOC-it/SOC-it.pdf
	AM	A. Mendelsohn. "FPGA-Based I/O Board Offloads Windows PC's." Product of the Week on Chipcenter.com, May 22, 2003.
	AN	

EXAMINER	DATE CONSIDERED
	2/5/04

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to Applicant.